

TRANSMITTAL

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Stylesheet Version v1.1.0

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|---|---------------------------------------|----------------------------|
| Title of Invention | DRAM Circuit and its Operation Method | |
| Application Number : | | |
| Date : | | |
| First Named Applicant: | KOHJI HOSOKAWA | |
| Confirmation Number: | | |
| Attorney Docket Number: | JP920020166US1 | |
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| Submitted By: | | Elec. Sign. |
| IRA D. BLECKER Registered Number: 29,894 | | IRA D. BLECKER/ S/ |
| | | Sign. Capacity Attorney |

| Documents being submitted: | Files |
|----------------------------|---|
| us-assignment | JP220020166US1-usassn.xml us-assignment.xsl us-assignment.dtd jp920020166us1assign1.tif jp920020166us1assign2.tif |
| us-request | JP220020166US1-usrequ.xml us-request.dtd us-request.xsl |
| us-fee-sheet | JP220020166US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd |
| application-body | JP920020166US1-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd jp920020166drw1of8.tif jp920020166drw2of8.tif jp920020166drw3of8.tif jp920020166drw4of8.tif jp920020166drw5of8.tif jp920020166drw6of8.tif jp920020166drw7of8.tif jp920020166drw8of8.tif |
| us-declaration | jp920020166us1dec1.tif |
| us-declaration | jp920020166us1dec2.tif |

us-declaration

jp920020166us1dec3.tif

Comments

ASSIGNEE NAME: INTERNATIONAL BUSINESS MACHINES CORPORATION ASSIGNEE ADDRESS:
ARMONK, NY 10504